

# 60A/61A/70A - SERIES

## 14 PIN DUAL-IN-LINE TTL ACTIVE DELAY LINE

### FEATURES

- 14-pin package.
- 10 equally spaced taps.
- TTL schottky interfaced.
- Delay times from 25-1000ns.



### ELECTRICAL CHARACTERISTICS

I <sub>IH</sub> Logic "1" Input Current	: 100uA Max
I <sub>IL</sub> Logic "0" Input Current	: -4mA Max
V <sub>OH</sub> Logic "1" Output Voltage	: 2.4V Min
V <sub>OL</sub> Logic "0" Output Voltage	: 0.4V Max
V <sub>IH</sub> Logic "1" Input Voltage	: 2.0V Min
V <sub>IL</sub> Logic "0" Input Voltage	: 0.8V Max
T <sub>A</sub> Operating Temperature	: 0°C to 70°C
N <sub>H</sub> Fanout "1" Output	: 20 TTL Load
N <sub>L</sub> Fanout "0" Output	: 10 TTL Load
Supply Current	: 150mA Typ.

### INPUT PULSE TEST CONDITION

Pulse Voltage	: 3.2V
Pulse Width	: 50ns Min. or 100%T <sub>d</sub>
Duty Cycle	: <50%
Pulse Rise Time	: 2ns (0.75 to 2.4V)
Time Delay Measured	: @1.5V I <sub>ecel</sub>
Supply Voltage V <sub>cc</sub>	: 5.0±0.25Vdc

### ELECTRICAL SPECIFICATIONS

Part Number	Delay Time T <sub>d</sub> (ns)	Tap Delay (ns)	Rise Time T <sub>r</sub> (ns) Max
60A/61A/70A-025	25±2nS	2.5±1	4
60A/61A/70A-050	50±5%	5±2	4
60A/61A/70A-075	75±5%	7.5±2	4
60A/61A/70A-100	100±5%	10±2	4
60A/61A/70A-150	150±5%	15±2	4
60A/61A/70A-200	200±5%	20±2	4
60A/61A/70A-250	250±5%	25±5%	4
60A/61A/70A-300	300±5%	30±5%	4
60A/61A/70A-500	500±5%	50±5%	4
60A/61A/70A-1000	1000±5%	100±5%	4

### PIN CONNECTIONS

