

45AF/47AF/55AF – SERIES

14 PINS LEADING & TRAILING FAST TTL ACTIVE DELAY LINE

FEATURES

- 14-pin package.
- 5 equally spaced taps.
- Fast TTL schottky interfaced.
- Delay times from 25-1000nS.
- TPLH and TPHL controlled.



ELECTRICAL CHARACTERISTICS

I _{IH} Logic "1" Input Current	: 20uA max
I _{IL} Logic "0" Input Current	: -1.6mA max
V _{OH} Logic "1" Output Voltage	: 2.5V min
V _{OL} Logic "0" Output Voltage	: 0.5V max
V _{IH} Logic "1" Input Voltage	: 2.0V min
V _{IL} Logic "0" Input Voltage	: 0.8V max
T _A Operating Temperature	: 0°C to 70°C
N _H Fanout "1" Output	: 20 TTL Load
N _L Fanout "0" Output	: 10 TTL Load
Supply Current	: 50mA Typ.

INPUT PULSE TEST CONDITION

Pulse Voltage	: 3.2V
Pulse Width	: 50ns min. or 100%T _d
Duty Cycle	: <50%
Pulse Rise Time	: 2ns (0.75 to 2.4V)
Time Delay Measured	: @1.5V I _{eccl}
Supply Voltage V _{cc}	: 5.0±0.25Vdc

ELECTRICAL SPECIFICATIONS

Part Number	Delay Time T _d (ns)	Tap Delay (ns)	Rise Time T _r (ns) max
45A/47A/55A-025F	25±2	5±2	4
45A/47A/55A-050F	50±5%	10±2	4
45A/47A/55A-075F	75±5%	15±2	4
45A/47A/55A-100F	100±5%	20±2	4
45A/47A/55A-150F	150±5%	30±2	4
45A/47A/55A-200F	200±5%	40±2	4
45A/47A/55A-250F	250±5%	50±5%	4
45A/47A/55A-300F	300±5%	60±5%	4
45A/47A/55A-500F	500±5%	100±5%	4
45A/47A/55A-1000F	1000±5%	200±5%	4

PIN CONNECTIONS

