

40P/41P – SERIES

8 PIN SINGLE-IN-LINE PASSIVE DELAY LINE

FEATURES

- 8-pin dip and surface mount package.
- 5 equally spaced taps.
- Wide operating temperature range.
- Total delays from 5-100ns.



ELECTRICAL CHARACTERISTICS

Total Delay	: 5ns to 100ns
Tap Delay	: 20% Increments Standard
Rise Time	: 20% Max of the total delay
Pulse attenuation	: See Table
Characteristic Impedance	: 50 to 200 Ω ($\pm 10\%$)
Distortion	: $\pm 10\%$ Max
Temperature Coefficient of Delay	: 100ppm/ $^{\circ}\text{C}$ Max
Operating Temperature Range	: 0°C to $+70^{\circ}\text{C}$
Storage Temperature Range	: -65°C to $+130^{\circ}\text{C}$
Dielectric Strength	: 50VDC

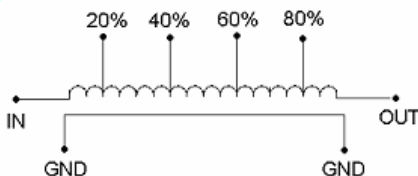
INPUT PULSE TEST CONDITION

Pulse Voltage	: 2.5V
Pulse Width	: 5Td
Pulse Rise Time	: 2ns (10% TO 90%)

ELECTRICAL SPECIFICATIONS

Part Number	Total Delay	Tap Delay (ns)	Rise Time Max (ns)	Attenuation (%)	Impedance (Ω)
40P/41P-00551XX	5 \pm 0.5	1 \pm 0.3	3	3	50
40P/41P-01051XX	10 \pm 1	2 \pm 0.4	4	3	50
40P/41P-02551XX	25 \pm 2	5 \pm 0.8	7	3	50
40P/41P-05051XX	50 \pm 2.5	10 \pm 1	15	3	50
40P/41P-07551XX	75 \pm 3.7	15 \pm 1	20	3	50
40P/41P-10051XX	100 \pm 5	20 \pm 1	28	3	50
40P/41P-00512XX	5 \pm 0.5	1 \pm 0.3	3	5	100
40P/41P-01012XX	10 \pm 1	2 \pm 0.4	4	5	100
40P/41P-02512XX	25 \pm 2	5 \pm 0.8	7	5	100
40P/41P-05012XX	50 \pm 2.5	10 \pm 1	15	5	100
40P/41P-07512XX	75 \pm 3.5	15 \pm 1	20	5	100
40P/41P-10012XX	100 \pm 5	20 \pm 1	28	5	100
40P/41P-00522XX	5 \pm 0.5	1 \pm 0.3	3	6	200
40P/41P-01022XX	10 \pm 1	2 \pm 0.4	4	6	200
40P/41P-02522XX	25 \pm 2	5 \pm 0.8	7	6	200
40P/41P-05022XX	50 \pm 2.5	10 \pm 1	15	6	200
40P/41P-07522XX	75 \pm 3.7	15 \pm 1	20	6	200
40P/41P-10022XX	100 \pm 5	20 \pm 1	28	6	200

PIN CONNECTION



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TABLE I PIN-NUMBER SEQUENCE							
CODE	IN	20%	40%	60%	80%	OUT	GROUND
STANDARD	2	3	4	5	6	7	1,8
A	1	2	3	4	6	7	4,8
B	1	7	3	6	4	5	8
C	7	2	6	3	5	4	1,8
TABLE III							
MOUNTING HEIGHT							
CODE				DIMENSION (MAX)			
A				.200(5.08)			
B				.250(6.35)			
C				.300(7.62)			