

# 40A/41A/49A/50A - SERIES

## 14 PIN DUAL-IN-LINE TTL ACTIVE DELAY LINE

### FEATURES

- 14-pin package
- 5 equally spaced taps.
- TTL schottky interfaced.
- Delay times from 25-1000nS.



### ELECTRICAL CHARACTERISTICS

IIH Logic"1" Input Current	: 50uA Max
IIL Logic"0" Input Current	: -2mA Max
VOH Logic"1" Output Voltage	: 2.7V Min
VOL Logic"0" Output Voltage	: 0.5V Max
VIH Logic"1" Input Voltage	: 2.0V Min
VIL Logic"0" Input Voltage	: 0.8V Max
TA Operating Temperature	: 0°C to 70°C
NH Fanout"1" Output	: 20 TTL Load
NL Fanout"0" Output	: 10 TTL Load
Supply Current	: 75mA Typ.

### INPUT PULSE TEST CONDITION

Pulse Voltage	: 3.2V
Pulse Width	: 50ns Min. or 100%Td
Duty Cycle	: <50%
Pulse Rise Time	: 2ns (0.75 to 2.4V)
Time Delay Measured	: @1.5V Iccel
Supply Voltage Vcc	: 5.0±0.25Vdc

### ELECTRICAL SPECIFICATIONS

Part Number	Delay Time Td (ns)	Tap Delay (ns)	Rise Time Tr (ns) Max
40A/41A/49A/50A-025	25±2	5±2	4
40A/41A/49A/50A-050	50±5%	10±2	4
40A/41A/49A/50A-075	75±5%	15±2	4
40A/41A/49A/50A-100	100±5%	20±2	4
40A/41A/49A/50A-125	125±5%	25±2	4
40A/41A/49A/50A-150	150±5%	30±2	4
40A/41A/49A/50A-200	200±5%	40±2	4
40A/41A/49A/50A-250	250±5%	50±5%	4
40A/41A/49A/50A-500	500±5%	100±5%	4
40A/41A/49A/50A-1000	1000±5%	200±5%	4

### PIN CONNECTIONS

