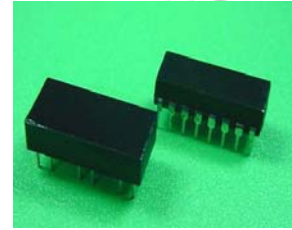


20T/21T/22T/23T – SERIES ISOLATION TRANSFORMERS FOR ETHERNET

FEATURES

- Low leakage inductance and coupling capacitance for faster rise times
- Small form factor, 50 mil lead spacing
- Through hole, auto-insertable, Surface Mount, 16-pin package



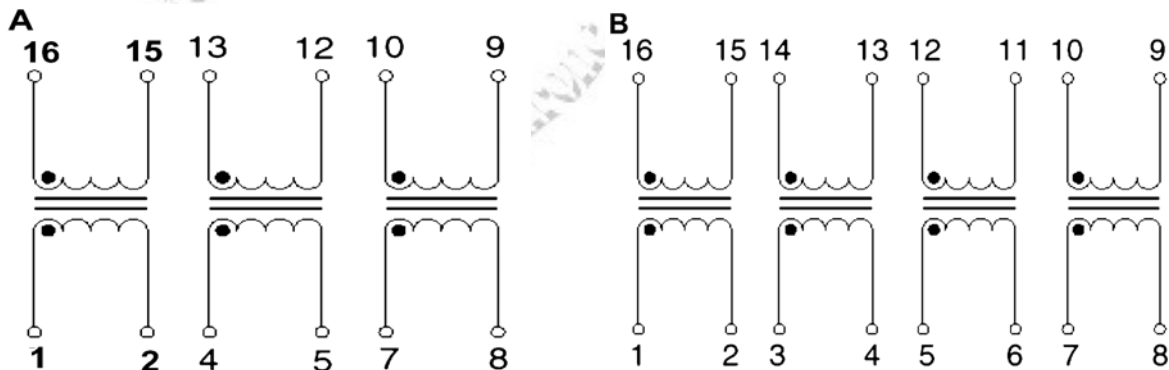
TEST CONDITIONS

- Inductance 100KHZ/20mV
- Leakage Inductance 100KHZ/20mV
- Interwinding Capacitance 100KHZ/20mV

ELECTRICAL SPECIFICATIONS @25°C-Operating temperature 0°C TO 70°C

Part Number	Tums Ratio (±5%)	Primary Pins	Primary Sine Wave OCL (uH) (+/- 20%)	Primary ET-Constant (uH) Min	Rise Time	Interwinding Capacitance CWW' (uF) Max	Leakage Inductance LL' (uH) Max	DCR (Ω) Max	Schem	Hipot Vrms
23T-4001A2	1:1	1-2/4-5/7-8	40(min)	2.1	3.0	8	0.20	0.25	A	2000
23T-7501A2	1:1	1-2/4-5/7-8	75	1.8	3.0	8	0.20	0.30	A	2000
23T-1011A2	1:1	1-2/4-5/7-8	100	1.8	3.0	9	0.20	0.30	A	2000
23T-1511A2	1:1	1-2/4-5/7-8	150	1.2	3.0	12	0.20	0.30	A	2000
23T-3511A2	1:1	1-2/4-5/7-8	350	2.6	3.5	16	0.30	0.35	A	2000
22T-7501A	1:1	1-2/4-5/7-8	75	2.1	3.0	10	0.20	0.20	A	500
20T-7501A2	1:1	1-2/4-5/7-8	75	2.4	3.5	10	0.25	0.30	A	2000
22T-7501A2	1:1	1-2/4-5/7-8	75	2.1	3.0	10	0.20	0.20	A	2000
22T-1011A	1:1	1-2/4-5/7-8	100	2.1	3.0	10	0.20	0.30	A	500
20T-1011A2	1:1	1-2/4-5/7-8	100	2.1	3.5	10	0.25	0.40	A	2000
22T-1011A2	1:1	1-2/4-5/7-8	100	2.1	3.0	10	0.20	0.30	A	2000
22T-1511A	1:1	1-2/4-5/7-8	150	2.1	3.5	12	0.20	0.30	A	500
20T-1511A2	1:1	1-2/4-5/7-8	150	3.0	3.0	15	0.20	0.45	A	2000

SCHEMATICS

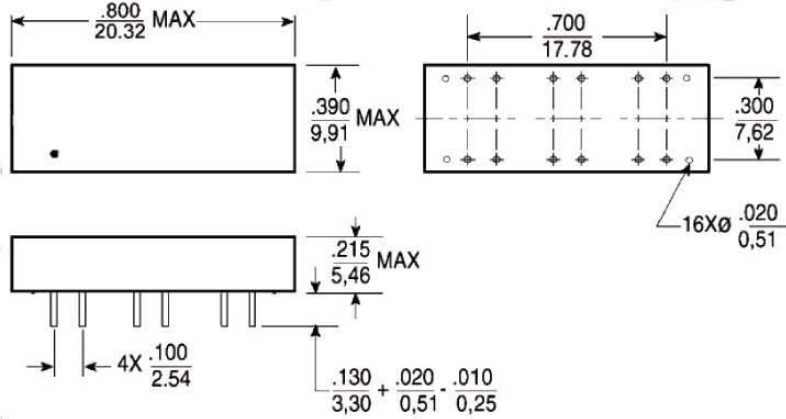


20T/21T/22T/23T – SERIES ISOLATION TRANSFORMERS FOR ETHERNET

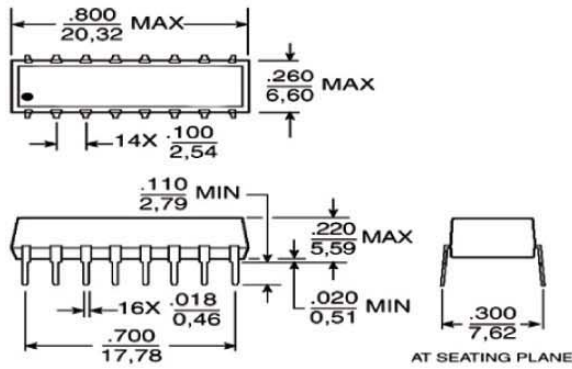
MARKINGS AND DIMENSIONS

Dimensions: inches/mm unless otherwise specified, all tolerances are $\pm 0.010/\pm 0.25$

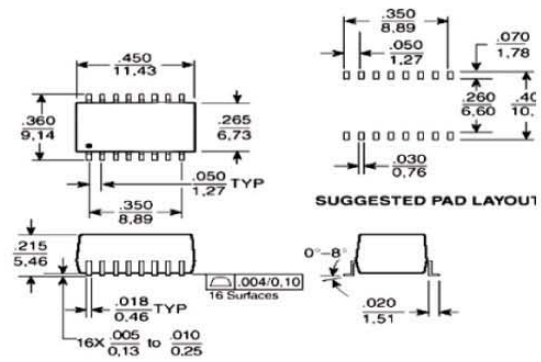
20T



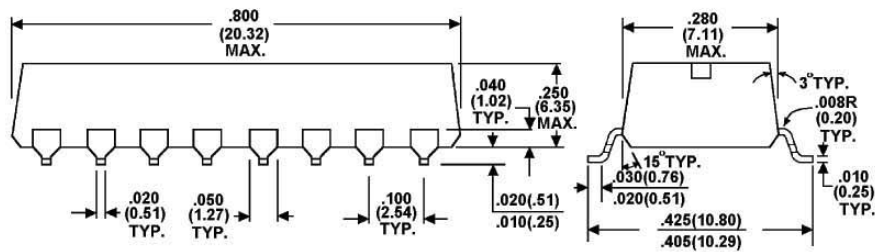
22T



23T



SUGGESTED PAD LAYOUT



UNIT:inch(mm)