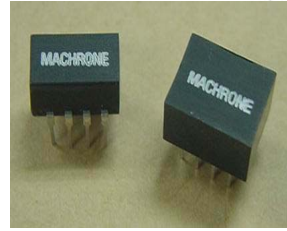


10A/11A/20A - SERIES

8 PIN DUAL-IN-LINE TTL ACTIVE DELAY LINE

FEATURES

- 8-pin package.
- 5 equally spaced taps.
- TTL schottky interfaced.
- Delay times from 25-1000ns.



ELECTRICAL CHARACTERISTICS

I _{IH} Logic "1" Input Current	: 50uA Max
I _{IL} Logic "0" Input Current	: -2mA Max
V _{OH} Logic "1" Output Voltage	: 2.7V Min
V _{OL} Logic "0" Output Voltage	: 0.5V Max
V _{IH} Logic "1" Input Voltage	: 2.0V Min
V _{IL} Logic "0" Input Voltage	: 0.8V Max
T _A Operating Temperature	: 0°C to 70°C
N _H Fanout "1" Output	: 20 TTL Load
N _L Fanout "0" Output	: 10 TTL Load
Supply Current	: 75mA Typ.

INPUT PULSE TEST CONDITION

Pulse Voltage	: 3.2V
Pulse Width	: 50ns Min. or 100%T _d
Duty Cycle	: <50%
Pulse Rise Time	: 2ns (0.75 to 2.4V)
Time Delay Measured	: @1.5V I _{eccl}
Supply Voltage V _{cc}	: 5.0±0.25Vdc

ELECTRICAL SPECIFICATIONS

Part Number	Delay Time T _d (ns)	Tap Delay (ns)	Rise Time T _r (ns) Max
10A/11A/20A-025	25±2	5±2	4
10A/11A/20A-050	50±5%	10±2	4
10A/11A/20A-075	75±5%	15±2	4
10A/11A/20A-100	100±5%	20±2	4
10A/11A/20A-150	150±5%	30±2	4
10A/11A/20A-200	200±5%	40±2	4
10A/11A/20A-250	250±5%	50±5%	4
10A/11A/20A-300	300±5%	60±5%	4
10A/11A/20A-500	500±5%	100±5%	4
10A/11A/20A-1000	1000±5%	200±5%	4

PIN CONNECTIONS

